

FIG. 1

201220122508001

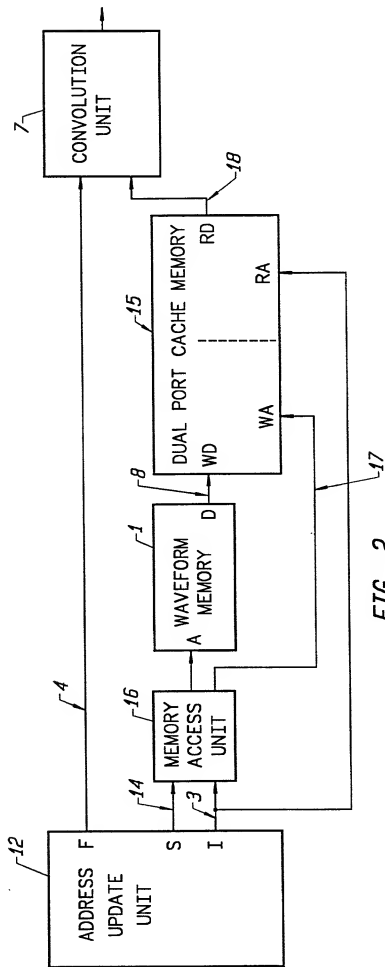


FIG. 2

201220" 22508001

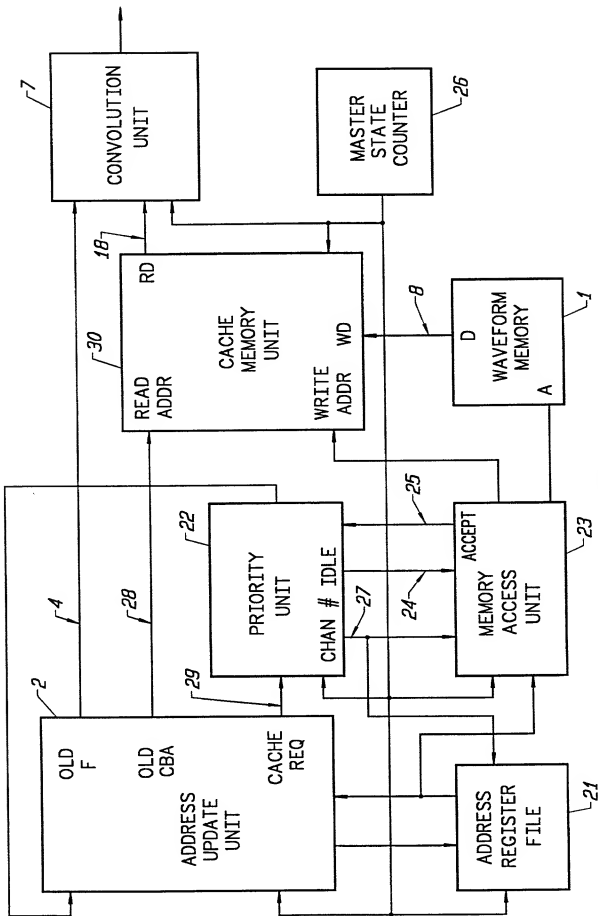


FIG. 3

201220-22503001

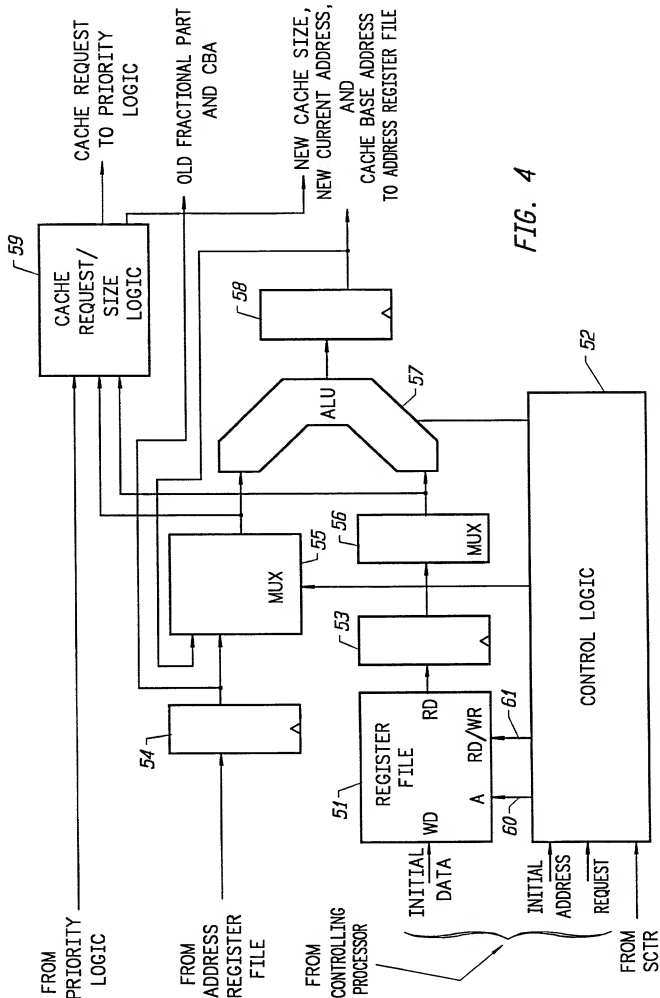


FIG. 4

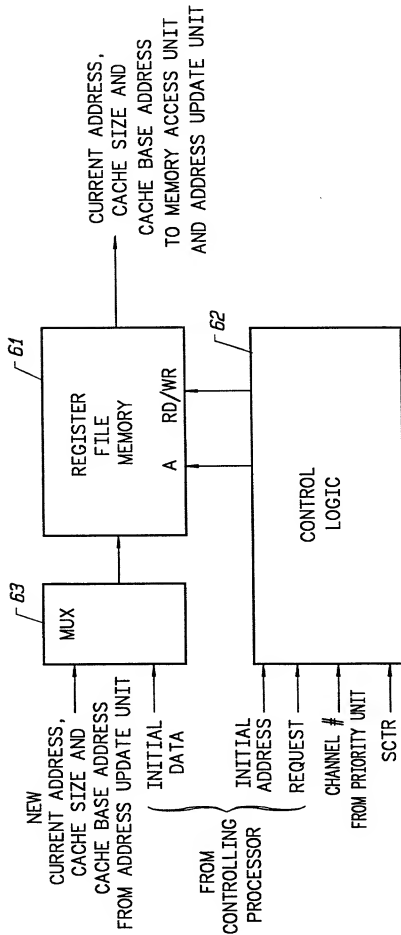


FIG. 5

201220 22508001

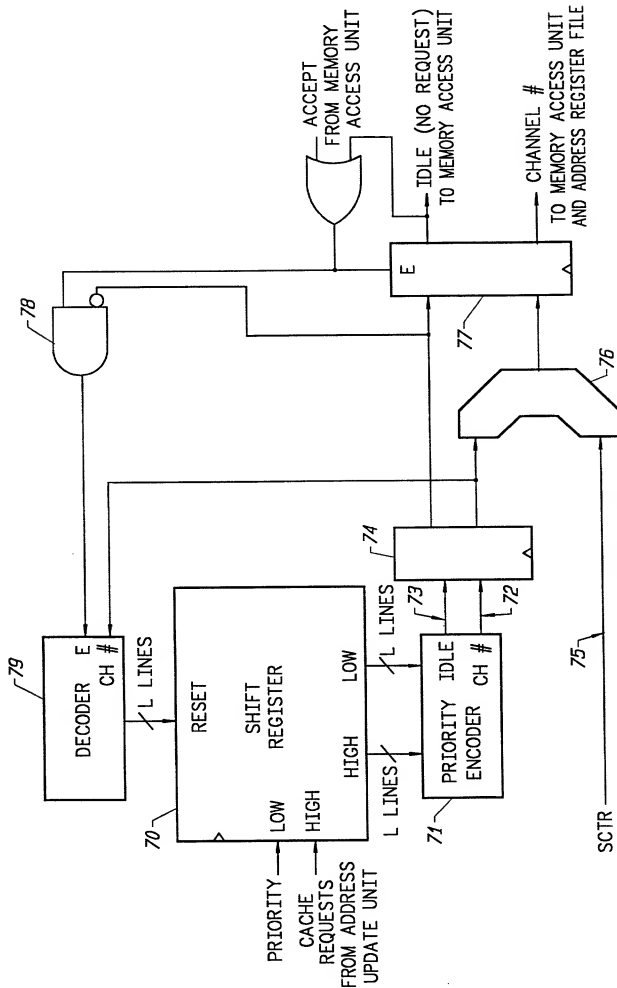


FIG. 6

201220-22508001



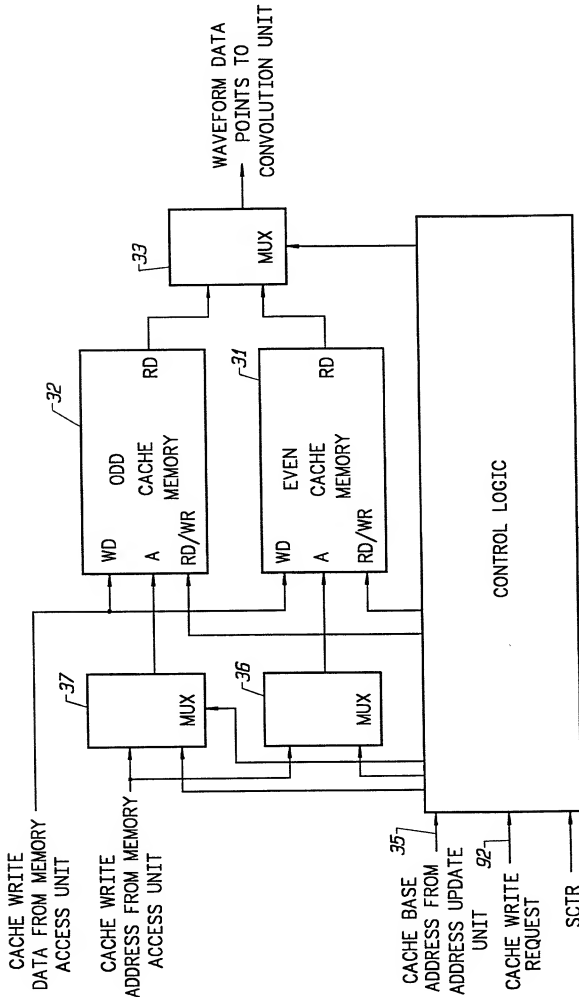


FIG. 8

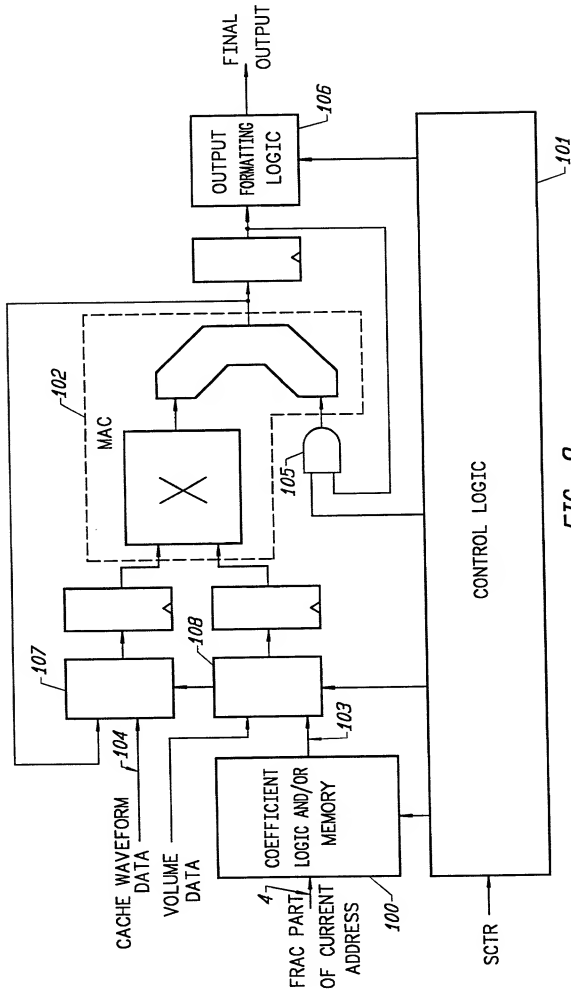


FIG. 9

201220-22508001

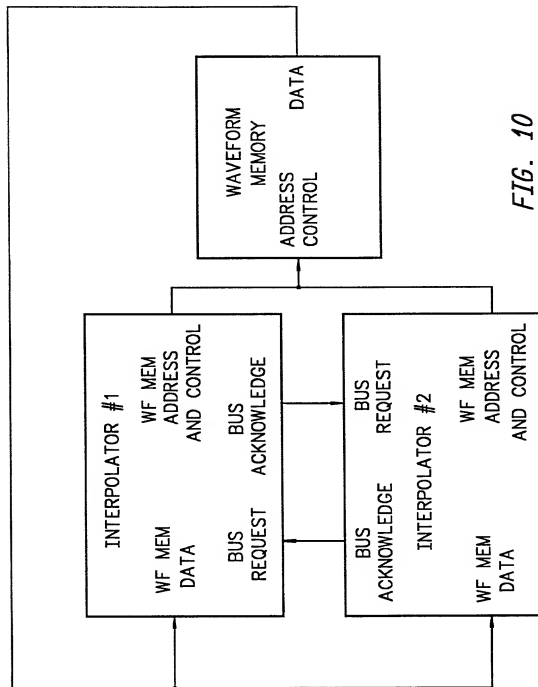


FIG. 10

201220 22508001

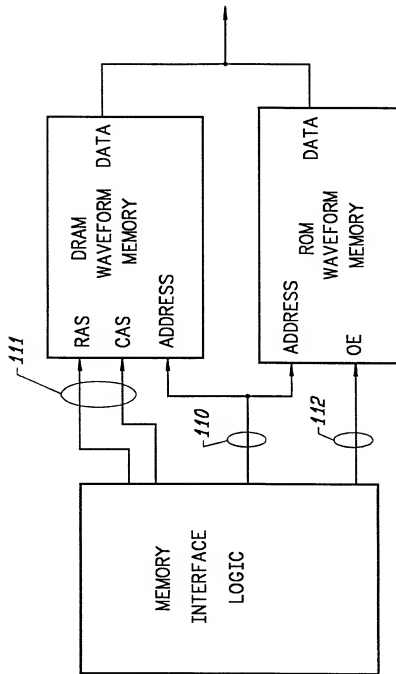


FIG. 11

201220' 22508001

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STATE COUNTER (8 PER CHANNEL)	0	1	2	3	4	5	6	7	0(NEXT CHANNEL)
CBA ADDRESS	NEW VALID CACHE BASE ADDRESS-----								
CACHE RAW READ OPERATION	NEW VALID	READ POINT 0	READ POINT 1	READ POINT 2	READ POINT 3	READ POINT 4	READ POINT 5	DON'T CARE	NEW VALID CBA
CR READ OPERATION CBA EVEN	NXP	READ EVEN POINT	READ ODD POINT	READ EVEN POINT	READ ODD POINT	READ EVEN POINT	READ ODD POINT	READ POINT 6	NXP
CR READ OPERATION CBA ODD	NXP	READ ODD POINT	READ EVEN POINT	READ ODD POINT	READ EVEN POINT	READ ODD POINT	READ EVEN POINT	READ EVEN POINT	NXP
EVEN CR OPERATION CBA EVEN	NXP	READ ODD POINT	READ EVEN POINT	READ ODD POINT	READ EVEN POINT	READ ODD POINT	READ EVEN POINT	READ ODD POINT	NXP
ODD CR OPERATION CBA EVEN	AVAIL FOR WR	READ POINT 0	READ POINT 1	READ POINT 2	AVAIL FOR WR	READ POINT 4	AVAIL FOR WR	READ POINT 6	AVAIL FOR WR
EVEN CR OPERATION CBA ODD	AVAIL FOR WR	AVAIL FOR WR	READ POINT 1	AVAIL FOR WR	READ POINT 3	AVAIL FOR WR	READ POINT 5	AVAIL FOR WR	AVAIL FOR WR
ODD CR OPERATION CBA ODD	AVAIL FOR WR	AVAIL FOR WR	READ POINT 1	AVAIL FOR WR	READ POINT 3	AVAIL FOR WR	READ POINT 5	AVAIL FOR WR	AVAIL FOR WR
RAW EVEN WRITE CBA EVEN	WRITE	WAIT	WRITE	WAIT	WRITE	WAIT	WRITE	WAIT	WRITE
RAW ODD WRITE CBA EVEN	WRITE	WRITE	WAIT	WRITE	WAIT	WRITE	WAIT	WRITE	WRITE
RAW EVEN WRITE CBA ODD	WRITE	WRITE	WAIT	WRITE	WAIT	WRITE	WAIT	WRITE	WRITE
RAW ODD WRITE CBA ODD	WRITE	WAIT	WRITE	WAIT	WRITE	WAIT	WRITE	WAIT	WRITE

FIG. 12